

Form PTO 1449 U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE				APP DOCKET NO. 000687-00326		SERIAL NO. Not yet assigned	
LIST OF REFERENCES CITED BY APPLICANT				APPLICANT Rajeev Balasubramanian, et al.		10,764,688	
				FILING DATE January 27, 2004 (Herewith)		GROUP 2824 Not yet assigned	
U.S. PATENT DOCUMENTS							
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
ap	AA	5,491,806	02-1996	Horstmann, et al.	711	207	
ap	AB	5,910,927	06-1999	Hamamoto, et al.	365	230.03	
ap	AC	6,349,363 B2	02/2002	Cai, et al.	711	230.03	
ap	AD	6,442,078 B1	08/2002	Arimoto, Kazutami	365	189.08	
ap	AE	5,774,471	06/1998	Jiang, Yong H.	714	711	
ap	AF	6,118,723	09/2000	Agata, et al.	365	230.03	
	AG						
	AH						
	AI						
	AJ						
	AK						
	AL						
	AM						
	AN						
	AO						
	AP						
	AQ						
	AR						
FOREIGN PATENT DOCUMENTS							
		DOCUMENT NUMBER	DATE	COUNTRY	TRANSLATION YES NO		
	AS						
	AT						
	AU						
	AV						
OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, etc.)							
ap	AW	Dynamic Memory Hierarchy Performance Optimization, R. BALASUBRAMONIAN et al., International Symposium on Computer Architecture, July 11, 2000.					
	AX						
	AY						
Examiner <i>Cash Phung</i>					Date Considered 8/3/04		

*Examiner: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.